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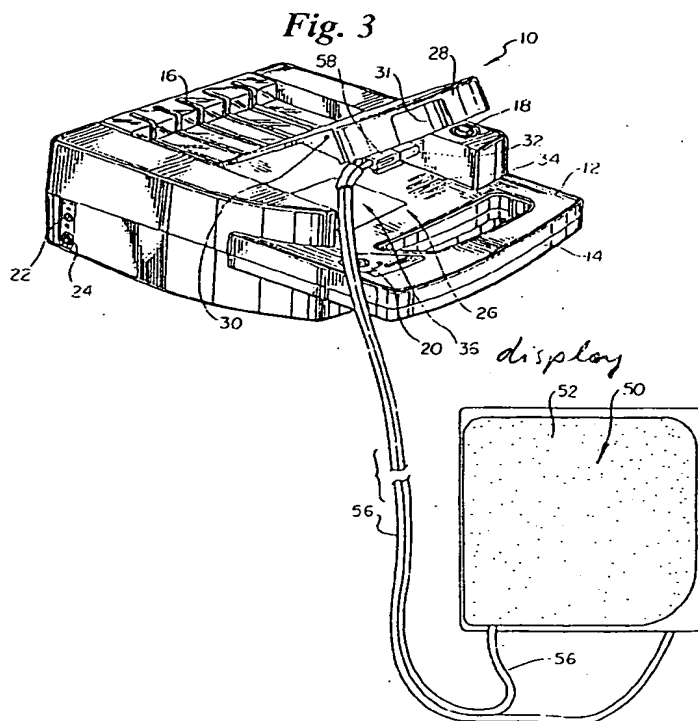
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(54) Automated external defibrillator with self-test system

(57) An automated external defibrillator which automatically performs self-tests on a daily and weekly basis. Tested functions include the presence and interconnection of defibrillator electrodes, battery charge state

and the operability of the high voltage circuit. Visual and audible indicators are actuated to alert an operator if faults are identified. A record of each self-test is stored in memory, and can be subsequently retrieved through a communications port.



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Description

The present invention relates generally to automated external defibrillators. In particular, the present invention is an automated external defibrillator with a self-test system for automatically and periodically testing the operational status of the defibrillator, and for providing service alerts if faults are identified.

Automated external defibrillators or AEDs are used by police officers, paramedics and other first-responder emergency medical technicians to resuscitate cardiac arrest patients. It is important that the AEDs carried by these technicians be continuously operational and ready for use on a moments notice. To help ensure a high level of confidence that they will be operational when needed, AEDs should be periodically checked and tested by the technicians, and corrective maintenance performed if any faults are identified. By way of example, AED functions and components that should be periodically checked and tested include the charge state of the batteries, the presence of electrodes and the ability of the device to charge and deliver defibrillation pulses. The American Heart Association recommends that AEDs be tested daily or at the beginning of a shift.

Unfortunately, for a variety of reasons the frequency at which AEDs are tested by the technicians that will be using them varies. Since studies have shown that the chances of successfully resuscitating a patient decrease approximately ten percent per minute following cardiac arrest, the consequences of first-responder medical technicians arriving at a rescue location with a nonfunctional AED can be severe. There is, therefore, a continuing need for AEDs capable of being reliably maintained in a functional state.

The invention is defined in the accompanying independent claims. Preferred features are recited in the dependent claims.

The present invention is an improved automated external defibrillator (AED). One embodiment of the defibrillator includes a digital control system with self-test means for periodically and automatically performing self-tests of one or more defibrillator components. If a malfunctioning component is identified, the self-test means actuates an audible alarm or other maintenance indicator to alert an operator. Tested functions include the presence and interconnection of defibrillator electrodes, battery charge state, the functionality of the high voltage circuit and the functionality of the digital control system. Some functions are self-tested daily, while others are self-tested weekly.

Figure 1 is a perspective view of an automated external defibrillator (AED) in accordance with the present invention, with the electrode compartment lid closed;

Figure 2 is a perspective view of the AED shown in Figure 1, with the electrode compartment lid

opened and the packaged electrodes positioned therein;

Figure 3 is a perspective view of the AED shown in Figure 2, with the electrodes removed from the electrode compartment and the package;

Figure 4 is a detailed view of the diagnostic display panel in the electrode compartment;

Figure 5 is a detailed view of the unpackaged electrodes positioned on the release liner;

Figure 6 is a block diagram of the electrical system of the AED shown in Figure 1; and

Figure 7 is an illustration of the instruction and safety label on the inside surface of the electrode compartment lid.

A semi-automatic, automated external defibrillator (AED) 10 in accordance with the present invention is illustrated generally in Figures 1-3. As shown, defibrillator 10 includes a plastic case 12 with a carrying handle 14 on the top portion. A battery compartment (not visible) in the bottom portion of the defibrillator 10 is enclosed by a semi-transparent battery cover 16. An illuminatable rescue switch 18, visual maintenance indicator 20, data communication port 22 and charging port 24 are located on the outside of case 12 for easy access by an operator.

Case 12 also includes an electrode compartment 26 between handle 14 and battery cover 16. The electrode compartment 26 is enclosed by lid 28 which is mounted to the case 12 by hinges (not visible). A friction-type releasable latch including pins 30 holds lid 28 closed when defibrillator 10 is not in use. The finger-receiving recess 31 in the lid 28 is grasped to open the lid and access the electrode compartment 26. An electrode connector 32, speaker 34 and diagnostic display panel 36 are located on case 12 within the electrode compartment 26. As shown in Figure 4, diagnostic display panel 36 includes visual "Call for Service" indicator light 38, "Check Electrode" indicator light 40, "Check 9 Volt Battery" indicator light 42, "Check 12 Volt Battery" indicator light 44 and "Power" indicator light 46. Resume switch 48 and resume indicator light 49 are also located on diagnostic panel 36. An instruction and safety label such as that shown in Figure 7 is located on the inside surface of electrode compartment lid 28.

A pair of defibrillator electrodes 50 which can be used with defibrillator 10 are shown in Figures 3 and 5. Electrodes 50 each include a flexible polymer backing layer 52 and a patient-engaging layer 54 of conductive adhesive which overlays the backing layer. A current-dispersing flexible conductive sheet (not visible) is located between the backing layer 52 and patient-engaging layer 54. Insulated lead wires 56 extend from each electrode 50, and have a first end connected to the conductive sheet and a second end connected to connector 58. Connector 58 is configured to releasably mate with the electrode connector 32 in electrode compartment 26. Electrodes 50 are sealed within a polymer or polymer-metal laminate package 60 such as that shown in

Figure 2. Lead wires 56 and connector 58 extend from package 60. The layers 54 of electrodes 50 are affixed in a face-to-face orientation to opposite sides of a release liner 61 within package 60. The release liner 61 is perforated with a number of apertures, so the electrodes 50 are electrically coupled to one another within the package 60. A relatively low resistance electrical circuit is thereby established between the ends of the lead wires 56 at connector 58. As shown in Figure 2, electrode package 60 is positioned within electrode compartment 26, and connector 58 plugged into the connector 32 in the compartment, to maintain defibrillator 10 in a ready-to-use state. Packaged electrodes 50 having the above-described characteristics are disclosed in the Gilman et al. U.S. Patent 5,402,884, and are commercially available from Survivalink of Minnetonka, Minnesota.

Figure 5 is a block diagram of the electrical system 70 of defibrillator 10. The overall operation of defibrillator 10 is controlled by a digital microprocessor-based control system 72 which includes a processor 74 interfaced to program memory 76, data memory 77, event memory 78 and real time clock 79. The operating program executed by processor 74 is stored in program memory 76. Data memory 77 is used by processor 74 as a scratch pad memory during the execution of the operating program. Electrical power is provided by a rechargeable twelve volt lead-acid cartridge battery 80 and a nine volt battery 82 which are removably positioned within the battery compartment and connected to power generation circuit 84. During normal operation, power generation circuit 84 generates regulated $\pm 5V$, 3.3V and 12V (actually about 13.3V) supplies with the power provided by the twelve volt battery 80. Nine volt battery 82 functions as a back-up battery to power components of electrical system 70 during the execution of self-tests and to activate maintenance indicators and alarms (as described below) if the twelve volt battery 80 is low on charge. Although not separately shown in Figure 5, power generation circuit 84 includes voltage level sensing circuits which are coupled to processor 74. The voltage level sensing circuits provide low battery level signals to processor 74 whenever the voltage levels of batteries 80 or 82 are less than predetermined values such as 12.3V and 8V, respectively.

The $\pm 5V$ supply is used to power the control system 72 and most other electrical components of electrical system 70. The 3.3V supply is coupled to nonvolatile event memory 78 in which, as is described in greater detail below, data representative of the patient's cardiac rhythm and the rescue mode operation of defibrillator 10 are stored. A high voltage generation circuit 86 is connected to receive the 12V supply. Charging port 24 is coupled to power generation circuit 84, enabling twelve volt battery 80 to be connected to a twelve volt vehicle battery (not shown) or a 120VAC charger (also not shown) and recharged while mounted within the defibrillator 12. Alternatively battery 80 can be removed

from defibrillator 10 and charged in a stand-alone charger (not shown). Defibrillator 10 cannot be operated when a charger is connected to charge port 24. Circuitry (not separately shown) within power generation circuit 84 senses the interconnection of port 24 to a charger, and provides a charger connected signal to processor 74 when a connected charger is sensed.

Power generation circuit 84 is also connected to power control circuit 88 and processor 74. Power control circuit 88 is connected to lid switch 90, watch dog timer 92, real time clock 79 and processor 74. Lid switch 90 is a magnetic reed relay switch in one embodiment, and provides signals to processor 74 indicating whether lid 28 is open or closed. Data communication port 22 is coupled to processor 74 for two-way serial data transfer using an RS-232 protocol. Rescue switch 18, maintenance indicator 20, rescue switch light 19, resume switch 48, indicator lights 38, 40, 42, 44, 46 and 49 of diagnostic display panel 36, voice circuit 94 and piezoelectric audible alarm 96 are also connected to processor 74. Voice circuit 94 is connected to the speaker 34. In response to voice prompt control signals from processor 74, circuit 94 and speaker 34 generate the audible voice prompts described below.

High voltage generation circuit 86 is also connected to and controlled by processor 74. Circuits such as 86 are generally known, and disclosed, for example, in the commonly assigned Persson et al. U.S. Patent 5,405,361, which is hereby incorporated by reference. In response to charge control signals provided by the processor 74, high voltage generation circuit 86 is operated in a charge mode during which one set of semiconductor switches (not separately shown) cause a plurality of capacitors (also not shown), to be charged in parallel to the 12V potential supplied by power generation circuit 84. Once charged, and in response to discharge control signals provided by processor 74, high voltage generation circuit 86 is operated in a discharge mode during which the capacitors are discharged in series by another set of semiconductor switches (not separately shown) to produce the high voltage defibrillation pulses. The defibrillation pulses are applied to the patient through electrode connector 32 which is connected to the high voltage generation circuit 86. Under certain circumstances described below, processor 74 causes high voltage generation circuit 86 to be discharged through an internal resistive load 98 rather than connector 32.

Impedance measuring circuit 100 is connected to electrode connector 32 and real time clock 79, and is interfaced to processor 74 through analog-to-digital (A/D) converter 102. The impedance measuring circuit 100 receives a clock signal having a predetermined magnitude from clock 79, and applies the signal to electrodes 50 through connector 32. The magnitude of the clock signal received back from the electrodes 50 through connector 32 is monitored by impedance measuring circuit 100. An impedance signal representative of the im-

pedance present across electrode connector 32 is then generated by circuit 100 as a function of the ratio of the magnitudes of the applied and received clock signals (i.e., the attenuation of the applied signal). For example, if the electrodes 50 within package 60 are operational and the connector 58 is properly connected to connector 32 on defibrillator 10, a relatively low resistance (e.g., less than about ten ohms) should be present across the connector 32. If the conductive adhesive on the electrodes 50 is dried out, the connector 58 is not properly connected to connector 32, or the electrodes are not properly positioned on the patient, a relatively high resistance (e.g., greater than about one hundred ohms) will be present across the connector 32. The resistance across connector 32 will be between about fifty and eighty ohms when fresh electrodes 50 are properly positioned on the patient with good electrical contacts. The impedance signal representative of the impedance measured by circuit 100 is digitized by A/D converter 102 and provided to processor 74.

Defibrillator 10 also includes electrocardiogram (EKG) filter and amplifier 104 which is connected between electrode connector 32 and A/D converter 102. The EKG or cardiac rhythm of the patient is processed by filter and amplifier 104 in a conventional manner, and digitized by A/D converter 102 before being coupled to processor 74.

The rescue mode operation of defibrillator 10 is initiated when an operator opens lid 28 to access the electrode package 60. The opening of the lid 28 is detected by lid switch 90, which effectively functions as an on/off switch. In response to this action, power control circuit 88 activates power generation circuit 84 and initiates rescue mode operation of processor 74. Processor 74 then begins its rescue mode operation by switching maintenance indicator 20 to a maintenance required state (e.g., a yellow visual display in one embodiment), flashing rescue switch light 19 and the indicator lights on diagnostic display panel 36, and performing a lid opened self-test. Processor 74 also initiates the generation of an audible voice prompt "To attempt a rescue, disconnect charger." if a charger is connected to charge port 24 when lid 28 is opened.

During the lid opened self-test, processor 74 checks: 1) the charge state of batteries 80 and 82, 2) the interconnection and operability of electrodes 50, 3) the state of event memory 78, 4) the functionality of real time clock 79, and 5) the functionality of A/D converter 102. The charge states of batteries 80 and 82 are checked by monitoring the voltage level signals provided by power generation circuit 84. If batteries 80 and/or 82 are determined to have a low charge, lights 44 and/or 42, respectively, on diagnostic display panel 36 are illuminated by processor 74. The interconnection and operability of the electrodes 50 is checked by monitoring the impedance signals provided by impedance measuring circuit 100. If the package 60 of electrodes 50 is missing or unplugged from connector 32, or if the elec-

trodes are damaged (e.g., dried out), processor 74 will illuminate the indicator light 40 on diagnostic display panel 36. As described in greater detail below, data representative of the operation of defibrillator 10 during a rescue and the patient's cardiac rhythm are stored in event memory 78. The data can be subsequently retrieved from event memory 78 through communications port 22, and the memory cleared. During the lid opened self-test, processor 74 accesses the event memory 78 to determine whether data from a previous rescue is still stored in the memory. If so, processor 74 causes light 49 on diagnostic panel 36 to be illuminated, and initiates the generation of a "Press resume button to clear memory and continue." voice prompt. If resume switch 18 is pressed by the operator following the activation of these indicators, processor 74 clears event memory 78 and proceeds with its rescue mode operation. The functionality of real time clock 79 and A/D converter 102 are checked by monitoring the outputs of these circuit elements for expected signals. Diagnostic display panel light 38 is illuminated by processor 74 if faults are identified in either of clock 79 or converter 102.

If the lid opened self-test is successfully completed, processor 74 switches maintenance indicator 20 to an operational state (e.g., a black color in one embodiment), and initiates the generation of an audible "Place electrodes." voice prompt. In response to this voice prompt, and following the instructions on the inside of lid 28, the operator should remove electrode package 60 from compartment 26, open the package, peel electrodes 50 from the release liner 61 and place the electrodes on the patient's chest. While this action is being performed, processor 74 monitors the impedance signals received through A/D converter 102 to determine whether the impedance across the electrodes indicates that they have been properly positioned on the patient. If the correct impedance is not measured, processor 74 initiates the generation of a "Check electrodes." voice prompt.

After detecting an impedance indicating the proper placement of electrodes 50, and without further action by the operator (i.e., automatically), processor 74 begins a first analyze sequence by initiating the generation of a "Do not touch patient. Analyzing rhythm." voice prompt, and analyzing the patient's cardiac rhythm. In one embodiment, processor 74 collects and analyzes a nine second segment of the patient's cardiac rhythm. The cardiac rhythm analysis program executed by processor 74 is stored in program memory 76. Algorithms of the type implemented by the rhythm analysis program are generally known and disclosed, for example, in the W. A. Tacker Jr. book *Defibrillation of the Heart*, 1994. If the processor 74 determines that the patient has a nonshockable cardiac rhythm that is not susceptible to treatment by defibrillation pulses (e.g., no pulse rather than a fibrillating rhythm), it initiates the generation of a "Check pulse. If no pulse, give CPR." voice prompt. One minute after this voice prompt, processor 74 repeats the

initiation of the "Do not touch patient. Analyzing rhythm. " voice prompt and the associated cardiac rhythm analysis.

When a shockable cardiac rhythm is detected, processor 74 begins a first charge sequence by initiating the generation of a "Charging." voice prompt, and causes high voltage generation circuit 86 to operate in the charge mode. When the high voltage generation circuit 86 is charged, processor 74 begins a first shock sequence by initiating the generation of a "Stand clear. Push flashing button to rescue." voice prompt, and the flashing illumination of rescue switch light 19. The operator actuation of rescue switch 18 will then cause processor 74 to operate high voltage generation circuit 86 in the discharge mode, and results in the application of a defibrillation pulse to the patient to complete the first series of analyze/charge/shock sequences. In one embodiment, the first defibrillation pulse delivered by defibrillator 10 has an energy content of about two hundred joules.

Following the first series of analyze/charge/shock sequences, processor 74 times out a short pause of about five seconds to allow the heart to reestablish its cardiac rhythm before beginning a second series of analyze/charge/shock sequences. The second series of analyze/charge/shock sequences is identical to the first series described above, except the energy content of the defibrillation pulse can be about two hundred joules or three hundred joules. If the second series of analyze/charge/shock sequences ends with the delivery of a defibrillation pulse, processor 74 again times out a short pause of about five second before beginning a third analyze/charge/shock sequence. The third series is also identical to the first series, but processor 74 controls the high voltage generation circuit 86 in such a manner as to cause the defibrillation pulse delivered upon the actuation of the rescue switch 18 to have an energy content of about three hundred and sixty joules.

Following the delivery of a defibrillation pulse at the end of the third series of analyze/charge/shock sequences, or after identifying a nonshockable cardiac rhythm, processor 74 initiates the generation of a "Check Pulse. If no pulse, give CPR." voice prompt. Processor 74 then times a one minute CPR period to complete a first set of three series of analyze/charge/shock sequences. Rescue mode operation then continues with additional sets of three series of analyze/charge/shock sequences of the type described above (all with three hundred and sixty joule pulses). Processor 74 ends rescue mode operation of defibrillator 10 when a total of nine series of analyze/charge/shock sequences have been performed, or lid 28 is closed.

Throughout the analyze, charge and shock sequences, processor 74 monitors the impedance present across connector 32 to determine whether electrodes 50 remain properly positioned on the patient. If the monitored impedance is out of range (e.g., too high if the electrodes have come off the patient, or too low if short-

ed), processor 74 initiates the generation of a "Check Electrodes." voice prompt, and causes high voltage generation circuit 86 to discharge any charge that may be present through internal load 98. Rescue mode operation will resume when processor 74 determines that the electrodes have been properly repositioned on the patient.

Processor 74 initiates and performs a lid closed self-test when lid 28 is closed following rescue mode operation of the defibrillator 10. During the lid closed self-test processor 74 performs a comprehensive check of the status and functionality of defibrillator 10, including: 1) the state of event memory 78, 2) the functionality of real time clock 79, 3) the functionality of A/D converter 102, 4) the functionality of program memory 76, data memory 77 and event memory 78, 5) the charge state of batteries 80 and 82, and 6) the interconnection and operability of electrodes 50. The state of event memory 78, the state of batteries 80 and 82, the interconnection and operability of electrodes 50, and the functionality of clock 79 and A/D converter 102 are checked in a manner identical to that described above with reference to the lid opened self-test. Conventional memory test routines are implemented to check the functionality of program memory 76, data memory 77 and event memory 78. Light 38 on diagnostic display panel 36 is illuminated (when lid 28 is subsequently opened), and maintenance indicator 20 is switched to its maintenance required state by processor 74 if faults are identified during the lid closed self-test. No audible alarms are actuated if faults are identified in the charge state of batteries 80 or 82 or the interconnection or functionality of electrodes 50 during the lid closed self test. However, alarm 96 is actuated by processor 74 if other faults are identified during the lid opened self test.

A daily self-test is initiated and performed by processor 74 at a predetermined time each day (i.e., every twenty-four hours). During the daily self-test processor 74 performs all the component check operations described above that are performed during the lid opened and lid closed self-tests. In addition to illuminating the appropriate lights on diagnostic display panel 36, processor 74 switches maintenance indicator 20 to its maintenance required state and activates alarm 96 if faults are identified during the daily self-test.

Processor 74 also initiates and performs a weekly self-test at a predetermined time one day each week. During the weekly self-test processor 74 performs all the component check operations described above that are performed during the daily self-test. In addition, processor 74 causes high voltage generation circuit 86 to sequentially operate in its charge and discharge modes, with the charge being dumped to the internal load 98. While the high voltage generation circuit 86 is operating in the charge mode, processor 74 monitors the time required to charge the capacitors and the capacitor voltage. A fault is identified if either is out of nominal conditions. Maintenance indicator 20 and alarm 96 are actu-

ated in the manner described above if any faults are identified during the weekly self-test.

Watch dog timer 92 is set to time watch dog time-out periods of about thirty hours (i.e., a period greater than the twenty-four hour periods between daily self-tests), and is reset by processor 74 at the beginning of each daily self-test and each time lid 26 is opened. In the event control system 70 malfunctions and watch dog timer 92 times out, power control circuit 88 causes processor 74 to switch maintenance indicator 20 to the maintenance required state and to actuate alarm 96 to alert an operator to the fact that defibrillator 10 requires maintenance.

Data representative of the operation of defibrillator 10 and the monitored cardiac rhythm of the patient are stored in event memory 78 during rescue mode operation. Stored data representative of the operation of defibrillator 10 includes the real time of the occurrence of each of the following events: 1) the placement of electrodes 50 on the patient, 2) the initiation of the cardiac rhythm analysis voice prompt, 3) the initiation of the charging voice prompt, 4) the completion of the charge mode operation of high voltage generation circuit 86, and 5) the actuation of rescue switch 18. The actual time base of the patient's cardiac rhythm is also stored in memory 78. Following a rescue, the stored data can be retrieved from event memory 78 through the use of a personal computer (PC) (not shown) interfaced to communications port 22. Real time clock 79 can also be set through the use of a PC interfaced to communications port 22.

Upon the completion of each lid opened, lid closed, daily and weekly self-test, processor 74 causes a record of the self-test to be stored in event memory 78. Each stored record includes data representative of the date and time of the test and the results of the test. The test results are recorded in the form of a code or other description indicating whether all the functions, components and component status states passed the test, or indicating the nature of any identified faults. In one embodiment, only the records of the ten most recently performed tests are stored in memory 78. The stored self-test records can be retrieved from memory 78 through a PC interfaced to communications port 22.

Defibrillator 10 offers considerable advantages. In particular, the device is relatively easy to use. The lid-actuated on-off switch, voice prompts, "one button" rescue operation and other aspects of the operator interface help enable high-quality defibrillation rescues. The wide range of self-tests and diagnostic displays enable operators to conveniently and accurately assess the operational status of the defibrillator.

Although the present invention has been described with reference to preferred embodiments, those skilled in the art will recognize that changes can be made in form and detail without departing from the scope of the invention.

Claims

1. An automated external defibrillator, comprising:

a case;
electrode terminals configured for electrical interconnection to defibrillator electrodes;
a battery compartment and battery terminals in the case, the battery compartment and terminals configured for holding and electrical interconnection to one or more batteries;
a high voltage circuit coupled to the battery terminals and the electrode terminals, for generating defibrillation pulses and applying the pulses to the electrode terminals;
a maintenance indicator on the case; and
a digital control system coupled to the electrode terminals, battery terminals, high voltage circuit and maintenance indicator, and including self-test means for periodically and automatically performing a self-test of the functionality of one or more defibrillator components, and for actuating the maintenance indicator if a malfunctioning component is identified.

2. An automated external defibrillator configured for use with a packaged pair of electrodes electrically connected to one another within the package and including lead wires with connectors extending from the package, the defibrillator including:

a case;
electrode terminals configured for electrical interconnection to defibrillator electrode connectors;
a battery compartment and battery terminals in the case, the battery compartment and terminals configured for holding and electrical interconnection to one or more batteries;
a high voltage circuit coupled to the battery terminals and the electrode terminals, for generating defibrillation pulses and applying the pulses to the electrode terminals;
an impedance measuring circuit for measuring the impedance between the electrode terminals;
a battery level monitoring circuit for measuring the charge state of the batteries;
a maintenance indicator on the case; and
a digital control system coupled to the electrode terminals, battery terminals, high voltage circuit, impedance measuring circuit, battery level monitoring circuit and maintenance indicator, including:

self-test initiating means for periodically and automatically initiating defibrillator self-tests;

battery test means for checking the charge state of the batteries during self-tests, and for actuating the maintenance indicator when low battery charge states are identified;

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electrode connection test means for checking the electrical interconnection of electrodes to the electrode terminals as a function of the measured impedance between the electrode terminals during self-tests, and for actuating the maintenance indicator when disconnected electrode states are identified.

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3. The defibrillator of claim 1 wherein the self-test means includes daily self-test means for performing at least a daily self-test.

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4. The defibrillator of claim 1,2 or 3 wherein the self-test means includes weekly self-test means for performing at least a weekly self-test.

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5. The defibrillator of claim 1,2,3 or 4 wherein the maintenance indicator includes an audible indicator and/or a visual indicator.

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6. The defibrillator of any of claims 1 to 5 wherein the self-test means includes means for checking the charge state of the batteries.

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7. The defibrillator of any of claims 1 to 6 wherein the self-test means includes means for checking the defibrillator electrodes.

8. The defibrillator of any of claims 1 to 7 wherein the self-test means includes means for checking the functionality of the high voltage circuit.

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9. The defibrillator of any of claims 1 to 8 wherein the self-test means includes means for checking the functionality of the digital control system.

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10. The defibrillator of any of claims 1 to 9 wherein the digital control system further includes:

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a memory; and

test record means for storing in the memory a record of the self-tests performed by the self-test means.

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11. The defibrillator of claim 2 wherein the digital control system further includes high voltage circuit test means for checking the functionality of the high voltage circuit during self-tests, and for actuating the maintenance indicator when high voltage circuit faults are identified.

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12. The defibrillator of claim 2 or 11 wherein the digital

control system further includes:

electronic memory; and

memory test means for checking the functionality of the electronic memory during self-tests, and for actuating the maintenance indicator when memory faults are identified.

Fig. 1

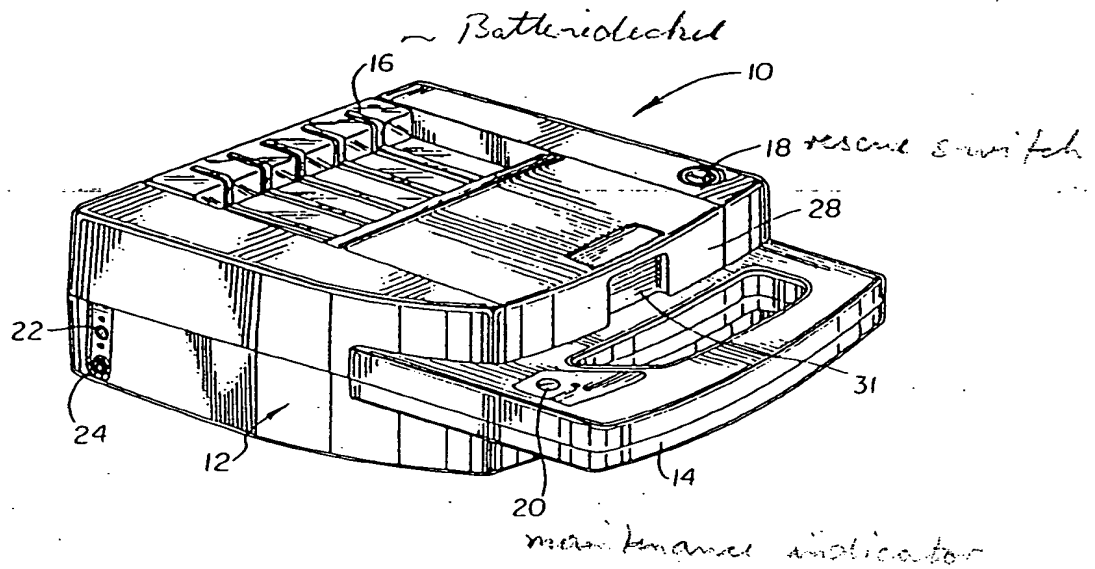


Fig. 2

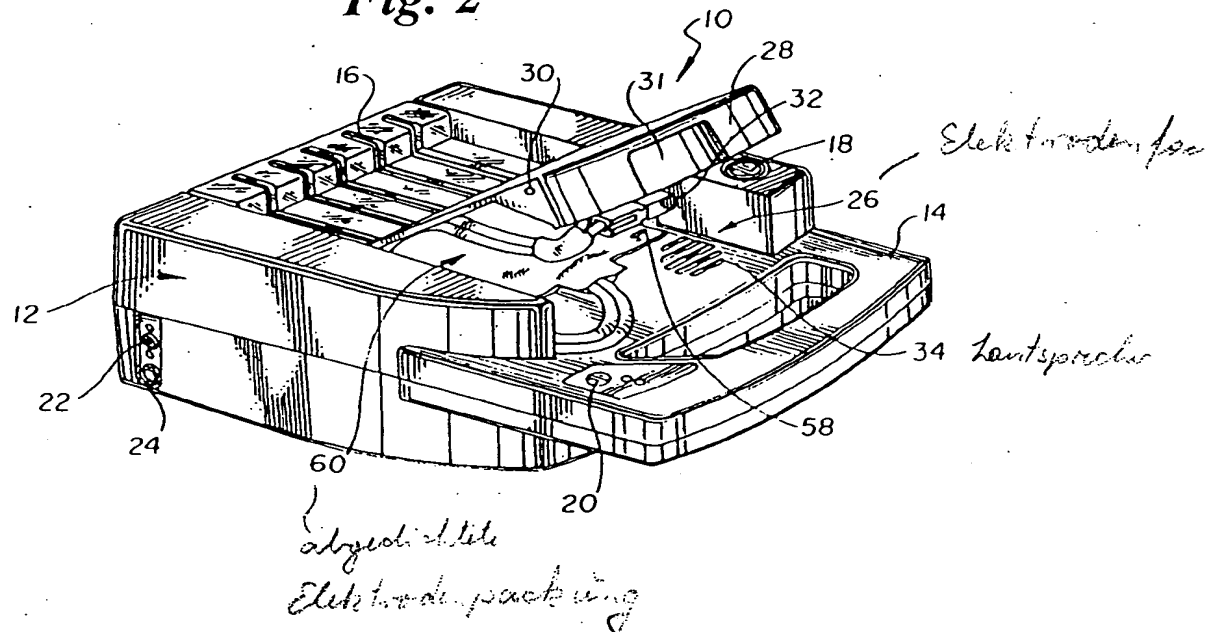


Fig. 3

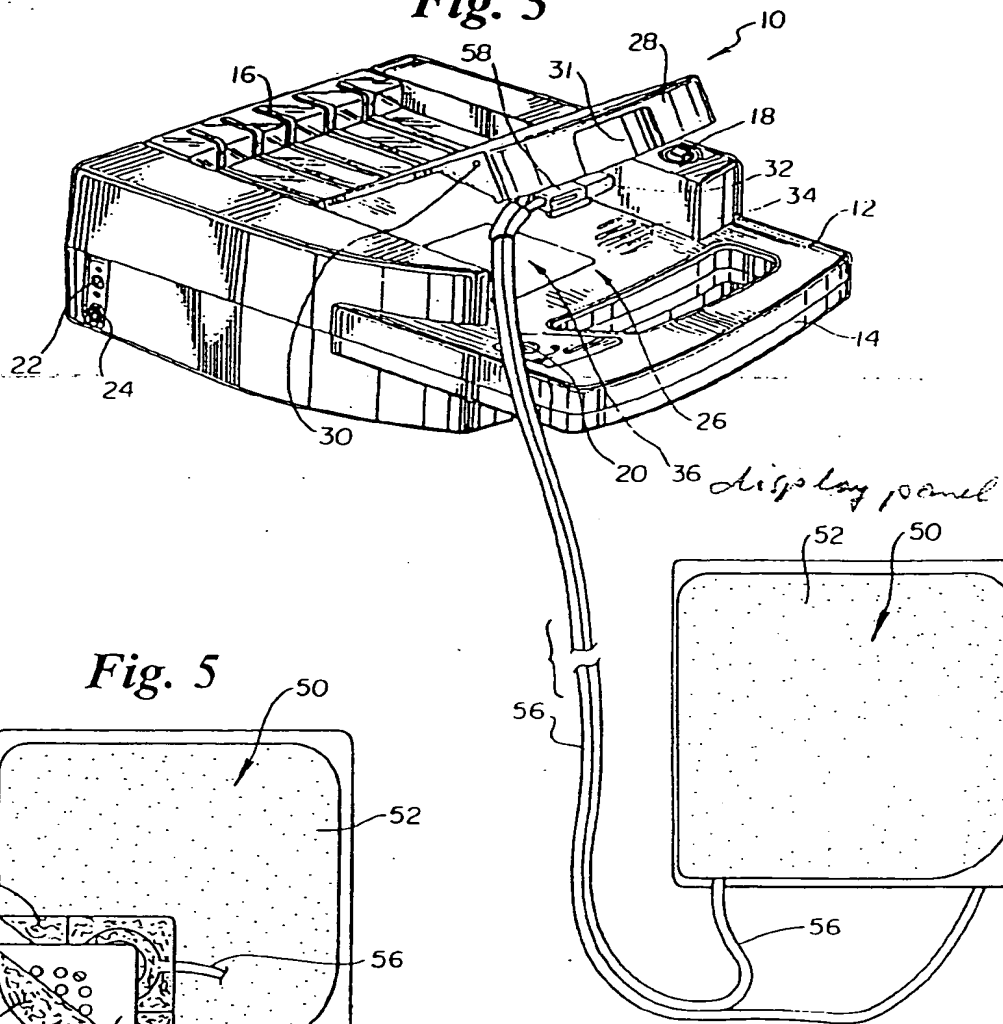


Fig. 5

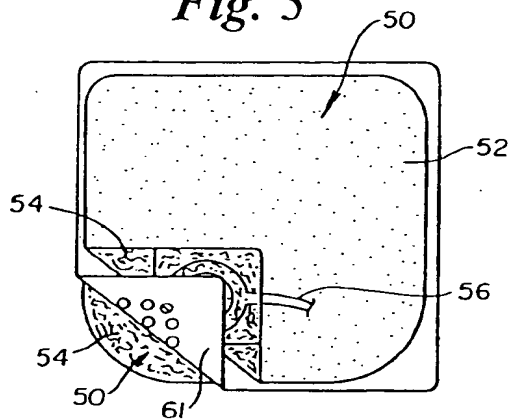
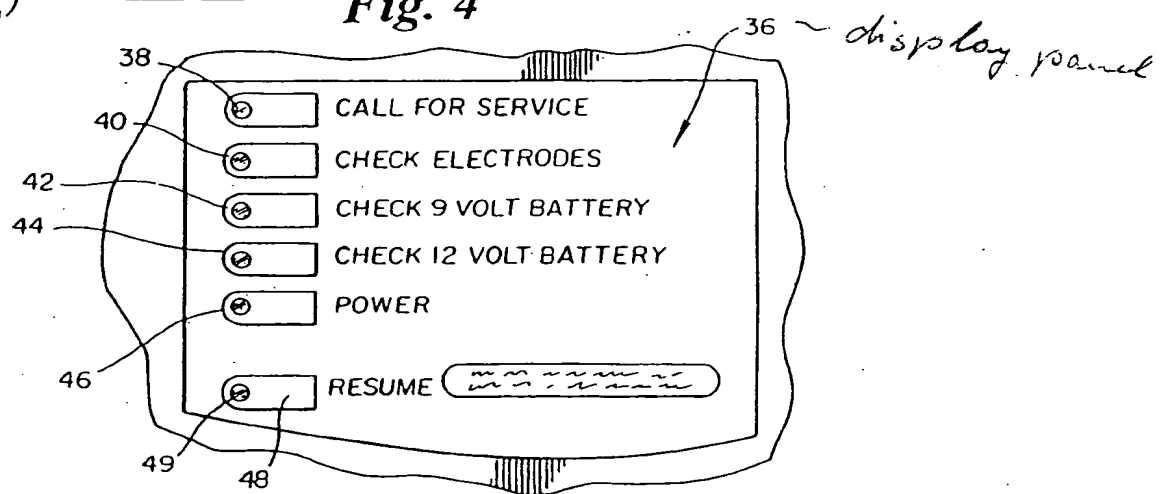


Fig. 4



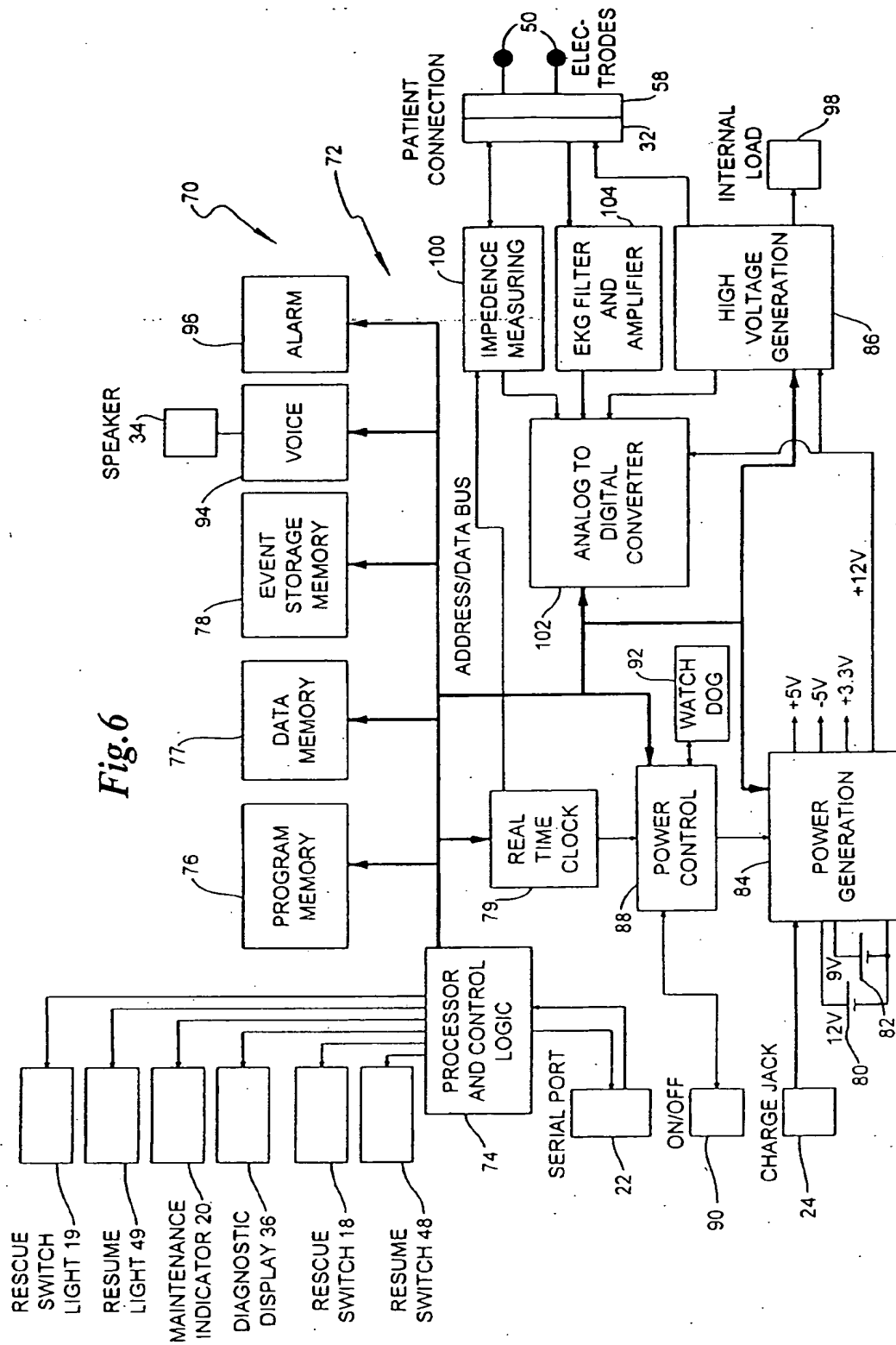


Fig. 7

